Faculty Details proforma for DU Web-site

(PLEASE FILL THIS IN AND Email it to websiteDU@du.ac.in and cc: director@ducc.du.ac.in

Title	Prof.	First Name	Mridula	Last Name	Gupta	Photograph
Designation	Senior Professor & HEAD					
Address	Department of Electronic Science University of Delhi, South Campus New Delhi – 110 021					
Phone No Office	91-11-24115580					
Residence	91-11-28522365					
Mobile	9810868230					
Email	mridula_du@yahoo.com, mridula@south.du.ac.in					
Web- Page						

Educational Qualifications		
Degree	Institution	Year
Ph.D.	University of Delhi	1998
M.Tech.	University of Delhi	1988
M.Sc. (Electronics)	University of Delhi	1986
B.Sc. (H) (Physics)	University of Delhi	1984
Any other qualification		

Career Profile

Organization	Designation	Duration	Role
Deptt. of Electronic Science, UDSC, INDIA	Senior Professor	18.07.2018- Till Date	Teaching & Research
	Professor	10.08.2006 -17.07.2018	Teaching & Research
	Associate Professor/ Reader	10.08.1998-09.08.2006	Teaching & Research
	Lecturer	18.10.1989 - 09.08.1998 01.08.1988 - 30.04.1989 (Ad-hoc)	Teaching & Research
C-DOT, Govt. of India	Research Engineer	01.05.1989 - 17.10.1989	Design of Transreceiver

Administrative Assignments

- Joint Dean Examination University of Delhi from 30.11.2021 to till date
- Joint Dean Student Welfare & staff advisor University of Delhi from 09.11.2020 to till date
- HEAD, Department of Electronics Science, University of Delhi from March 2020- till date
- Member of Examination Disciplinary Committee July 2021 to till date
- Chairperson of the DUCC Network Purchase Committee for the financial year 2020-2021, 24th Sep 2020-23rd Sep 2021

- **Member of DUCC Technical Advisory Committee for the financial year 2020-2021, 24th Sep 2020-23rd Sep 2021**
- **Chairperson of Telephone Advisory Committee for University of Delhi South Campus, 22nd June 2020 – Till Date**
- **Member of Governing body of Mata Sundri College, University of Delhi, 17th August 2020- till date**
- **Member of Governing body of Acharya Narendra Dev College, University of Delhi, 17th August 2020- till date**
- **Member of Board of Studies in Electronics, University of Jammu, 2018-2021**
- **Member of DUCC, University of Delhi, technical advisory committee, 2019-till date**
- **Coordinator B.Sc. (H) Electronics program, University of Delhi-2006-till date.**
- **DRC member of Department of Electronics Science, University of Delhi since 2006- till date**
- **BRS member of Department of Electronics Science, University of Delhi since 2006- till date**
- **Coordinator B.Sc. (H) Instrumentation program, University of Delhi-2006-2020.**
- **Provost- Geetanjali Hostel South Campus, University of Delhi, July 2014- January 2020.**
- **Member- Award and Fellow nomination standing committee of IEEE EDS Delhi Section Feb. 2015-Feb. 2017.**
- **Chairperson purchase committee- Department of Electronic Science, University of Delhi, South Campus, Feb-2013-Feb2017.**
- **VC Nominee-DRC, NSIT, Dwarka, New Delhi, Aug. 2015-July 2016.**
- **VC Nominee-DRC, IIC, UDSC, New Delhi-21, Jan. 2014-Jan. 2016.**
- **Member, General Council of Netaji Subhas Institute of Technology (NSIT), Dwarka, Feb. 2014-2016.**
- **Member of Governing body of Rajkumari Amrit Kaur College of Nursing April 2014- 2016.**
- **External expert of Board of Studies & Research in Department of Electronics & Communication Engineering of Guru Jambheshwar University of Science & Technology, Hisar, April 2014-April-2016.**
- **External expert of Board of Studies & Research in Department of Electronics & Communication Engineering of Bhagat Phool Singh Mahila Vishvidyalaya, Sonipat, Jan. 2014- Jan. 2016.**
- **Member & Treasure of Governing Body Deshbandhu College, Kalkaji, New Delhi, Sept. 2013-Sept. 2016.**
- **Placement coordinator, Department of Electronic Science, University of Delhi, South Campus, 2003-2014.**
- **Academic & Placement Co-coordinator at Institute of Informatics & Communication (Informatics Division), UDSC, INDIA, March-99 - Sept-02.**

Areas of Interest / Specialization

Modeling & characterization of submicron and deep submicron field effect devices. This includes the Si MOSFET, Tunnel FET, MESFET, GaAs MESFET, JunctionLess Transistor and AlGaN/GaN & GaAs/InP HEMT

Subjects Taught

33 years teaching experience of M.Sc. (Electronics), M.Sc. (Informatics) and M.Tech. (Microwave Electronics) classes.
Modern Communication Systems, Analog & Digital Circuit Design, Control Systems, Communication Theory

Research Guidance

List against each head (If applicable)

1. *Supervision of awarded Doctoral Thesis* - ***28***
2. *Supervision of Submitted Doctoral Thesis* - ***00***
3. *Supervision of Doctoral Thesis, under progress* - ***07***
4. *Supervision of awarded M.Phil dissertations* - ***Nil***
5. *Supervision of M.Phil dissertations, under progress* - ***Nil***

S.No.	Name	Year	Title of the Thesis
1.	Gupta, Ritesh	2003	Modeling, Characterization and Optimization of InAlAs/InGaAs

			Heterojunction, InP based High Electron Mobility Transistor (HEMT) For Microwave and Millimeter Wave Frequency Applications
2.	Singh, Adarsh	2004	Analytical Modeling, Analysis and Characterization of GaN MESFET for Optoelectronic Applications
3.	Sehgal, Amit	2007	Poly-Crystalline Silicon Thin Film Transistors: Modeling, Simulation and Characterization
4.	Mangla, Tina	2007	Modeling and Simulation Characterization of Nano Scale MOSFETs with Quantum Mechanical Effects and Gate Stack Engineering for ULSI
5.	Goel, Kirti	2007	Two Dimensional Analytical Modeling and Simulation of Non-Uniformly Doped Dual Martial Gate(DMG) and Triple Material Gate (TMG) MOSFET Structures
6.	Aggarwal, Sandeep	2008	Correlation & Enhancement of Circuit Parameters with Device Parameters of Different Metal-Insulator Geometric Single/ Double/Dual Gate 4H-SiC MESFET
7.	Kabra, Sneha	2008	Modeling Simulation and Characterization of GaN MESFET
8.	Parvesh	2008	Polarization dependent analysis and characterization of AlGaN/GaN HEMT
9.	Chaujar, Rishu	2008	Analytical modeling and simulation of gate electrode workfunction and dielectric engineered recessed channel MOSFET in Sub-100nm Regime
10.	Kumar, Sona P.	2010	Analysis , Modeling and Simulation of AlGaN/GaN Modular Doped Field Effect Transistor
11.	Sharma, Rupendra	2010	Two Dimensional Analytical Modeling and Simulation of Gate Misalignment Effect in Fully Depleted Double Gate MOSFET
12.	Aggarwal, Ruchika	2011	Modeling, Characterization and Simulation of AlGaN/GaN Metal Insulator Semiconductor Heterostructure Field Effect Transistor(MISHFET) for High Power Microwave Applications
13.	Rathi, Servin	2012	Modeling, Simulation & Characterization of Modified Different Gate Geometric Double Gate High Electron Mobility Transistor for High Power and High Frequency Applications with Two Separate/Common Gate Control
14.	Malik, Priyanka	2013	Analytical modeling and simulation of advanced MOSFET structures in sub-100nm regime
15.	Ghosh, Pujarini	2013	Capacitive Modeling Simulation and Characterization of Surrounded/Cylindrical Gate MOSFET (SGT/CGT) for High Frequency Applications
16.	Gautam, Rajni	2014	Analytical modeling and simulation of cylindrical gate all around MOSFET-reliability and sensor applications
17.	Bhattacharya, Monika	2014	Modeling, Simulation and Characterization of Noise in InAlAs/InGaAs Tied-geometry Double-gate High Electron Mobility Transistor for Millimeter-wave Applications
18.	Kumari, Vandana	2014	Impact of Dielectric Pocket on Different Gate Geometry MOSFET Architectures for Improved Analog and Digital Performance: Modeling and Simulation
19.	Narang, Rakhi	2014	Analytical Modeling and Simulation of Multiple Gate Geometry Tunneling Field Effect Transistors for Low Power Logic Circuit Design and Biosensing Applications

20.	Pratap, Yogesh	2016	Modelling and Characterization of all around Gate Nano-Scale MOSFETs
21.	Verma, Jay Hind Kumar	2017	Noise Analysis and Modeling of Cylindrical/surrounding gate MOSFET
22.	Ajay	2017	Analytical Modeling and Simulation study of BioFETs for label free electrical detection of biomolecules
23.	Kadam, Upasana	2018	Steep sub-threshold devices for energy efficient circuits: Modeling and simulation
24	Kumar, Manoj	2018	Analytical modeling, simulation and characterization of chottky barrier (SB) gate all around (GAA) MOSFET structures for low power applications
25.	Kumar, Sachin	2019	Novel Attributes of RingFET: A Modeling and Simulation Based Investigation
26.	Chander, Subhash	2019	Simulation, Modeling and Characterization of MMIC (Monolithic Microwave Integrated Circuit) Components
27.	Nitin	2019	Modeling Simulation and characterization of JunctionLess Accumulation mode GAA MOSFET for Digital Application
28.	Dubey, Avashesh	2022	Modeling and Simulation of steep sub-threshold Emerging Research Devices foe Energy Efficient Low Power Sensing Application.
29.	Yadav, Hina	In Progress (25th August 2017)	A High Gain FM Rectenna for RF power Harvesting Applications
30.	Singh, Preeti	In Progress (25th August 2017)	Modeling and Simulation of Advanced High electron Mobility Transistor (HEMT) for High Power Applications
31.	Sharma, Monika	In Progress (25th August 2017)	Modeling and Simulaion of Heterojunction Tunnel FET for low power circuit design and Optical Applications.
32	Khurana, Manisha	In Progress	Modeling and simulation of FETs under optical illumination
33	Anupama	In Progress	Modeling, simulation and performance comparison of AlGaN and GaN channel HEMTs
34	Gupta, Shweta	In Progress	Modeling & simulation based investigation of FET and TFET for biological sensing applications
35	Adham Morei Alkadi (FSR Category)	In Progress	Millimeter/Sub-Millimeter Wave Filters and Antennas for 5G/ Beyond 5G(6G) Applications

Publications Profile

List against each head(If applicable) (as Illustrated with examples)

1. *Books/Monographs (Authored/Edited)*
2. *Research papers published in Refereed/Peer Reviewed Journals*
3.
   a) *Research papers published in Academic Journals other than Refereed/Peer Reviewed Journals*
   b) *Research papers published in Refereed/Peer Reviewed Conferences*
   c) *Research papers Published in Conferences/Seminar other than Refereed/Peer Reviewed Conferences*
4. *Other publications (Edited works, Book reviews, Festschrift volumes, etc.)*

Research papers published in Refereed/Peer Reviewed journal (April 2021- March 2022)

1. Kumari Vandana, Gupta Mridula, and Saxena Manoj, 2021 "Impact of Non-Uniform Doping on the Reliability of Double Gate JunctionLess Transistor: A Numerical Investigation" IETE Technical Review, 2021.

2. Yadav Hina, Ray K P and Gupta Mridula, 2021 “Differential Multi-Resonator Stacked Microstrip Antenna For Wireless Energy Harvesting”, International Journal of RF and Microwave Computer-Aided Engineering, Vol 31, Issue 10, 2021 https://doi.org/10.1002/mmce.22828
3. Kumari Vandana, Gupta Mridula, and Saxena Manoj, 2021 "TCAD-Based Investigation of Double Gate JunctionLess Transistor for UV Photodetector," IEEE Transactions on Electron Devices, vol. 68, no. 6, pp. 2841-2847, 2021, doi: 10.1109/TED.2021.3075654.
4. Das S., Kumari V., Sehra K. , Gupta M. and Saxena M, 2021 "TCAD Based Investigation of Single Event Transient Effect in Double Channel AlGaN/GaN HEMT," in IEEE Transactions on Device and Materials Reliability, vol. 21, no. 3, pp. 416-423, Sept. 2021, doi: 10.1109/TDMR.2021.3103238.
5. Sital Shivani, Baliyan Anjli, Khular Sharma Enakshi, Gupta Mridula, 2021 "Optimization of multimode Fibers for surface Plasmon Resonance based Sensors under spectral and single wavelength intensity Interrogation”, Plasmonics, Springer pp. 1557-1963. October 2021 doi: 10.1007/s11468-021-01556-w
6. Anand Anupama, Reeta, Rawal Dipendra Singh, Narang Rakhi, Mishra Meena, Saxena Manoj and Gupta Mridula, 2021"A comparative study on the accuracy of small-signal equivalent circuit modeling for large gate periphery GaN HEMT with different source to drain length and gate width" Microelectronics Journal, Volume 118, pp. 105258, October 2021 doi: https://doi.org/10.1016/j.mejo.2021.105258.
7. Singh Preeti, Kumari Vandana, Saxena Manoj and Gupta Mridula, 2021 "TCAD investigation for Dual-Gate MISHEMT with Improved Linearity and Current Collapse for LNAs" IETE Technical Review, pp. 0256-4602, November 2021, doi: 10.1080/02564602.2021.1997362
8. Singh Preeti, Kumari Vandana, Saxena Manoj and Gupta Mridula, 2021 "E-mode All-GaN-Integrated Cascode MISHEMT with GaN/InAlGaN/GaN backbarrier for high power switching performance: Simulation Study" Superlattices and Microstructures, pp. 107118, December 2021, doi: https://doi.org/10.1016/j.spmi.2021.107118
9. Upasana, Saxena Manoj and Gupta Mridula, 2022 “Undoped Drain Graded Doping (UDGD) based TFET design: An innovative concept”, Superlattices and Microstructures, pp. 107147, January 2022, doi: 10.1016/j.spmi.2021.107147
10. Maneesha, Upasana, Saxena Manoj and Gupta Mridula, “Modeling and Simulation of Ge Absorber based Tunnel Field Effect Phototransistor at 1550 nm” IETE Technical Review, Accepted for Publication, Feb. 2022
11. Neha, Kumari Vandana, Gupta Mridula, Saxena Manoj, “Investigation of proton irradiated dual field plate AlGaN/GaN HEMTs: TCAD based assessment” Microelectronics Journal, Vol. 122, pp. 105405, April 2022.

International/National Conferences: (April 2021- March 2022)

1. Sehra K., Kumari V., Gupta M., Mishra M., Rawal D. S., and Saxena M. 2021 “Degradation Mechanisms in a Proton Irradiated HEMT with 3DEG Conduction and 3DHG as a Back Barrier,” 21st IEEE International Conference on Nanotechnology (IEEE – NANO), Montreal, Canada, July 2021, doi: 10.1109/NANO51122.2021.9514295.
2. Khurana Manisha, Upasana, Saxena Manoj and Gupta Mridula, 2021 “Impact of pocket doped Mg2Si/Si hetrojunction Ge gated TFET for low optical power detection at 1550 nm,” presented in XXIst International Workshop on the Physics of Semiconductor Devices (IWPSD 2021), 14-17 December, 2021, IIT Delhi, SSPL, DRDO, Delhi, India.
3. Khurana Manisha, Upasana, Saxena Manoj and Gupta Mridula, 2022 “Magnesium-Silicide (Mg2Si)/Silicon(Si) Heterojunction based TFET for Optical Detection at 1550 nm,” presented in 3rd IEEE Conference on VLSI Device, Circuit and System (IEEE VLSI DCS-2022), 26-27 February, 2022, Meghnad Saha Institute of Technology, Kolkata, India.
4. Anand Anupama, Rawal D. S., Narang Rakhi, Mishra Meena, Saxena Manoj and Gupta Mridula, 2021 “Effective Mobility Extraction of GaN-HEMT Using S-parameter,” presented in XXIst International Workshop on Physics of Semiconductor Devices (IWPSD 2021), 14-17 December, 2021, IIT Delhi, SSPL, DRDO, Delhi, India.
5. Sharma Monika, Narang Rakhi, Saxena Manoj and Gupta Mridula, 2021 “RF Analysis of Tapered Angle Hetero-junction Dopingless TFET for low power applications,” presented in XXIst International Workshop on the Physics of Semiconductor Devices (IWPSD 2021), 14-17 December, 2021, IIT Delhi, SSPL, DRDO, Delhi, India.
6. Sharma Monika, Narang Rakhi, Saxena Manoj and Gupta Mridula, 2022 “Single event transient effect on tapered angle hetero-junction dopingless TFET for radiation-sensitive applications,” presented in 3rd IEEE Conference on VLSI Device, Circuit and System (IEEE VLSI DCS-2022), 26-27 February, 2022, Meghnad Saha

Institute of Technology, Kolkata, India
7. Das Shreyasi, Sehra Khushwant, Kumari Vandana, Gupta Mridula and Saxena Manoj, 2021 "Comparative Investigation of Single and Double Channel AlGaN/GaN HEMTs for LNA" The XXIst International Workshop on the Physics of Semiconductor Devices (IWPSD 2021), 14-17th Dec. 2021.

Conference Organization/ Presentations (in the last three years)

List against each head (If applicable)

1. *Organization of a Conference*
   - Board of Chairman-Micro2020: 7th International Conference on Microelectronics, Circuits and Systems, 25th-26th July 2020, India
   - Member of Steering Committee-International Conference on Emerging Electronics (ICEE-2020), 26th – 28th Nov. 2020, New Delhi India.
   - Track Chair-INDICON 2020, 11-13th Dec. 2020
   - Track Chair-XXIst International Workshop on the Physics of Semiconductor Devices (IWPSD 2021)
   - Track Co-Chair-IEEE MTT-S International Microwave and RF Conference (IMaRC 2021), 18th December 2021
   - Track Chair IEEE Delhi Section International Conference on Electrical, Electronics and Computer Engineering (DELCON-2022), 11th February, 2022

2. *Participation as Paper/Poster Presenter*
3. *Participation as Invited Speaker*

   - Invited talk on 'Satellite Communication' at Acharya Narendra Dev College, on 31st January 2019.
   - Member of IEEE-WIE Networking and Penal Discussion at Department of Electronics and Communication Engineering, Amity University Noida, on 7th March 2019.
   - Invited talk on 'Advancement in Semiconductor Device Technology: Modeling and Simulation" at Maharaja Agrasen Institute of Technology, IP University on 4th April, 2019.
   - Invited talk at National Level Lecture Workshop on Women in Science: A Career in Science, on 'Advancement in Semiconductor Device Technology Feb 26-27, 2020, at Deen Dayal Upadhyaya College, University of Delhi.
   - Invited talk on 'Role of Teachers in National Education Policy (NEP): Awareness, Orientation, Challenges & Responses' at Human Resource Development Centre University of Delhi on 22nd March 2021
   - Keynote talk on "RESEARCH METHODOLOGY & INNOVATIONS IN ELECTRONIC SCIENCE" at Department of Electronics, DDU Gorakhpur University during 3rd - 9th July, 2021
   - Invited Talk in online FDP on "Trends & Issues on VLSI and Communication Engineering" at Department of ECE, GJUS, Hisar during 25th October – 30th October 2021.
   - Invited talk on "Small Signal Equivalent Circuit Model Parameter Extraction and Effective Mobility extraction of AlGaN/GaN HEMT using S-parameter" at IEEE VLSI DCS 2022, 3rd IEEE Conference on VLSI Device, Circuit and System, 26th – 27th Feb. 2022, Kolkata, India

Research Projects (Major Grants/Research Collaboration)

1. **DU IOE project:** Qualification of Indium HEMT technology for space Applications (PI). (In Progress)

Awards and Distinctions

1. Received IEEE EDS best Chapter of the year award-2017 (on the basis of work done in 2016)
2. Received IEEE EDS best Chapter of the year award-2013 (on the basis of work done in 2012)
3. Received **University Gold Medal** in 1986 for being the **best candidate** in the M.Sc. (Electronics) examination.
4. **Stood first** in the order of merit in M.Tech. (Microwave Electronics) examination in 1988.

5. Awarded **Nehru Centenary Common Wealth Fellowship (UK)** for three years in 1990.

Association With Professional Bodies

1. *Editing*
2. *Reviewing*
   - IEEE Transactions on Electron Devices
   - IEEE Electron Device Letters
   - Microelectronics Reliability
   - Applied Physics letters
   - International Journal of Numerical Modeling (IJNM)
   - Journal of Semiconductor Technology and Science (JSTS)
   - IET-Circuit Device and Systems (CDS)
   - Material Science in Semiconductor Processing,
   - IEEE Transaction On Nanotechnology
3. *Advisory*
4. *Committees and Boards*
5. *Memberships*
   - **Senior Member IEEE**
   - **Fellow** – The Institution of Electronics and Telecommunication Engineers.
   - **Life Member** – Semiconductor Society of India.
6. *Office Bearer*
   - **SRC Vice chair- IEEE EDS Region 10 from Ja. 2022- till date**
   - **President- Women In engineering (WIE) IEEE Delhi Section Affinity Group 2016-2018**
   - **Life Member & Secretary** – Society for Microelectronics and VLSI, India.
   - **Chairman –** IEEE-EDS Delhi Chapter (2011-2016 & 2019-2021)
   - **Chairperson-** IEEE Women In Engineering Delhi Chapter

Other Activities

Have been actively engaged in various activities of the Department (April 2021-March 2022)

- Lecture Series
  - Webinar on PCB Manufacturing and Assemblyon 07-09-2021, Virtual (Online)
  - IEEE EDS Distinguished Lecturer Talk on Near/in Sensor computing for neuromorphic machine vision by Dr.Yang Chai, The Hong Kong Polytechnic University on 15-09-2021, Virtual (Online)
  - Two-day Online lecture series on Fundamentals and Applications of Technology Driven Sensors from 24-09-2021 to 25-09-2021, Virtual (Online)
  - Distinguished Alumni lecture Series on "Antennas for RF Energy harvesting system design" by Dr. Nasimuddin, Scientist, SRO, Institute for Infocomm Research, A*STAR and "Phased array and Gimbel version seekers" by Dr. Yogesh Verma, Head - AD & AESA Seekers Division, Research Centre Imarat, DRDO Hyderabad on 29-09-2021 Virtual (Online)
  - One day webinar on Electronic devices and their applications on 06-10-2021, Virtual (Online)
  - IEEE EDS Distinguished Lecturer Talk on "Nanoelectronics to Nanotechnology: More Moore and more

than Moore"by Professor Durga Misra, New Jersey Institute of Technology (NJIT), Newark, USA on 20-10-2021, Virtual (Online)

- Technical talk on “Design of Superconducting Synchronous Machines for Low Speed Applications” by Dr David Torrey, Senior Principal Engineer in the Electric Power organization at General Electric Global Research on 21-10-2021, Virtual (Online)
- Distinguished Alumni lecture Series on "Application of GaN based devices in RF Electronics“ by Dr. meena Mishra, SSPL, DRDO and "Microwaves in medical fields including neutralizing COVID-19 virus" by Prof. K. P. Ray, DIAT, Pune on 22-10-21, virtual (online)
- IEEE EDS Distinguished Lecturer Talk on “3D Integration: above and beyond Moore’s law” Professor Jesús A. del Alamo, Massachusetts Institute of Technology, 29-10-2021, Virtual (Online)
- IEEE EDS Distinguished Lecturer Talk on “Differentiated Silicon for Non-terrestrial Broadband Internet addressing the digital divide” by Dr. Anirban Bandyopadhyay, Senior Director, Strategic Applications, GLOBALFOUNDRIES, Inc., US, 12-11-2021, Virtual (Online)
- (Distinguished Alumni Lecture Series). Two talks were organized (i) Make in India and Opportunities in Defense Sector” by Sh. Vaibhav Agarwal, General Manager- International Business Astra Microwave Products Ltd. (ii) Analog RF Front End Architecture by Shri Shiv Dutt, RF Lead Engineer, Sterlite Technology Limited , 17-11-2021, Virtual (Online)
- IEEE EDS Distinguished Lecturer Talk on “CMOS Scaling: Negative Capacitance and Challenges Ahead” by Prof. Merlyne De Souza, University of Sheffield, 03-12-2021, Virtual (Online)
- Online Technical Lecture Talk on “5G- Be ready for the future” by Ms. Rekha Jain, Team Leader, Centre For Development of Telematics(C-DOT), 04-12-2021, Virtual (Online)
- Technical Talk (Distinguished Alumni Lecture Series) on “TV White Space technology: Opportunities and Challenges” by Sh. Pankaj Sharma, Senior Manager, Institute of Microelectronics (Agency for Science, Technology & Research) & Co-Founder, Whizpace), on 09-12-2021, Virtual (Online)
- International Symposium on History and Future of Transistors organized during 23-30 December, 2021.
- Technical talk - Distinguished Alumni Lecture Series by Sh. Raj Singh, Scientific Officer – H, Institute for Plasma Research, Gandhinagar on the topic on 13-01-2022
- One day Webinar on Advanced Semiconductor Devices for industrial applications on 20-01-2022
- Technical Talk (Distinguished Alumni Lecture Series on 6G: Testing next-generation sub-terahertz applications by Mr. Vishal Gupta on 21-01-2022
- IEEE EDS Distinguished Lecturer Talk on Landscape of Synaptic Weight Memories by Prof. Shimeng Yu, Georgia Institute of Technology, Atlanta, GA, USA, 02-02-2022, Virtual (Online)
- IEEE EDS Distinguished Lecturer Talk on
- Heterogeneous Integration for AI Architectures by Dr. Mukta G. Farooq, Distinguished Research Staff Member, IBM Research - TJ Watson & Albany, New York., 08-02-2022, Virtual (Online)
- IEEE Distinguished Lecture on Spintronics-Perspectives and Challenges, on 10-02-2022 Virtual (Online)

Signature of Faculty Member

- You are also requested to also give your complete resume as a DOC or PDF file to be attached as a link on your faculty page.